IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
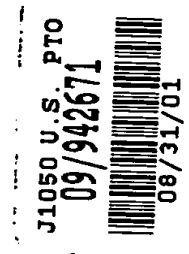
Applicant: Toshihiko OKAMURA

Title   ERROR-CORRECTING CODE DECODING METHOD AND
ERROR-CORRECTING CODE DECODING APPARATUS

Appl. No.: Unassigned

Filing Date: August 31, 2001

Examiner: Unassigned

Art Unit: Unassigned

INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR §1.56

Commissioner for Patents
Box PATENT APPLICATION
Washington, D.C. 20231

Sir:

Submitted herewith on Form PTO-1449 is a listing of documents known to Applicant in order to comply with Applicant's duty of disclosure pursuant to 37 CFR §1.56. A copy of each listed document is being submitted to comply with the provisions of 37 CFR §1.97 and §1.98.

The submission of any document herewith, which is not a statutory bar, is not intended as an admission that such document constitutes prior art against the claims of the present application or that such document is considered material to patentability as defined in 37 CFR §1.56(b). Applicant does not waive any rights to take any action which would be appropriate to antedate or otherwise remove as a competent reference any document which is determined to be a prima facie art reference against the claims of the present application.

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TIMING OF THE DISCLOSURE

The listed documents are being submitted in compliance with 37 CFR §1.97(b), within three (3) months of the filing date of the application.

RELEVANCE OF EACH DOCUMENT

The relevance of documents A8 through A13 is described in the present specification.

Document A1 is a U.S. equivalent of Document A2.

Document A2 relates to a Statistically Multiplexed Turbo Code Decoder.

Document A3 relates to a Encoding Method and Decoding Method.

Document A4 relates to a Reliability Information Generating Method and Repetitive Decoding Method.

Document A5 relates to a Transmitting Power Controller and Transmitter-Receiver.

Document A6 relates to a Soft Output Decoder.

Document A7 relates to a Error Correction Method.

English translations of the foreign-language documents are not readily available. However, the absence of such translations does not relieve the PTO from its duty to consider the submitted foreign language documents (37 CFR §1.98 and MPEP §609). English Abstracts are provided.

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Applicant respectfully requests that any listed document be considered by the Examiner and be made of record in the present application and that an initialed copy of Form PTO-1449 be returned in accordance with MPEP §609.

The Commissioner is hereby authorized to charge any additional fees which may be required regarding this application under 37 C.F.R. §§ 1.16-1.17, or credit any overpayment, to Deposit Account No. 19-0741. Should no proper payment be enclosed herewith, as by a check being in the wrong amount, unsigned, post-dated, otherwise improper or informal or even entirely missing, the Commissioner is authorized to charge the unpaid amount to Deposit Account No. 19-0741.

Respectfully submitted,

Date: <u>August 31, 2001</u>

By _____ Reg. No. 43,438

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Form PTO-1449 (MODIFIED)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. 040373-0306		SERIAL NO. Unassigned
INFORMATION DISCLOSURE CITATION		APPLICANT Toshihiko OKAMURA		
(Use several sheets if necessary)		FILING DATE August 31, 2001		GROUP ART UNIT Unassigned

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE IF APPROPRIATE
	A1	6,252,917	6/26/01	Freeman	375	340	

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION YES	NO
	A2	2000-101453	4/7/00	Japan				Abstract
	A3	2000-201085	7/18/00	Japan				Abstract
	A4	2000-091927	3/31/00	Japan				Abstract
	A5	2000-078029	3/14/00	Japan				Abstract
	A6	11-163742	6/18/99	Japan				Abstract
	A7	63-045921	2/26/88	Japan				Abstract

OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, Etc.)*

	A8	P. Robertson et al. "A Comparison of Optimal and Sub-optimal MAP Decoding Algorithms in the Log Domain", Institute for Communications Technology, pgs. 1009-1013, (1995).
	A9	G. Masera et al., "VLSI Architectures for Turbo Codes", IEEE Transactions on VLSI Systems, pgs. 369-379, (1999)
	A10	A.J. Viterbi, "An Intutive Justification and a Simplified Implementation of the MAP Decoder for Convolutional Codes", IEEE Journal on Selected Areas, pgs. 260-264, (1998).
	A11	S. Benedetto et al., "Soft Output Decoding Algorithms for Continuous Decoding of Parallel Concatenated Convolutional Codes", pgs. 112-117, ICC '96, (1996).
	A12	L.R. Bahl et al., "Optimal decoding of Linear Codes for Minimizing Symbol Error Rate", IEEE Transactions on Information Theroy, pgs. 284-287, (1974).
	A13	C. Berrou et al., "Near Shannon Limit Error-Correcting Coding and Decoding Turbo-Codes", ICC '95, pgs. 1064-1070, (1993)

EXAMINER	DATE CONSIDERED

* **EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include any copy of this form with next communication to applicant.**